

What is claimed is:

1. A method for using a solid-state memory device storing program code, the method comprising:

5 (a) connecting a solid-state memory device with a host device, the solid-state memory device storing program code;

(b) providing the program code to the host device; and

(c) with the host device, performing at least one of the following acts:

10 (c1) reading data stored in the solid-state memory device using the program code;

(c2) storing data in the solid-state memory device using the program code.

2. The invention of Claim 1, wherein (b) comprises automatically providing the program code to the host device.

3. The invention of Claim 1, wherein (c) comprises, with the host device, storing data in the solid-state memory device using the program code.

20 4. The invention of Claim 1 further comprising, before act (a), providing the solid-state memory device to a data storage device and storing data in the solid-state memory device with the data storage device.

25 5. The invention of Claim 4, wherein (c) comprises with the host device reading the data stored by the data storage device using the program code.

6. A solid-state memory device comprising:
a first portion storing program code; and
a second portion;

wherein the program code is operative to enable a host device connected with the solid-state memory device to perform at least one of the following acts: read data stored in the second portion of the solid-state memory device using the program code or store data in the second portion of the solid-state memory device using the program code.

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7. The invention of Claim 6, wherein the program code is operative to enable a host device connected with the solid-state memory device to store data in the second portion of the solid-state memory device using the program code.

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8. The invention of Claim 1 or 6, wherein the program code is written in a hardware-independent language.

9. The invention of Claim 8, wherein the hardware-independent language comprises Java.

10. The invention of Claim 1 or 6, wherein the solid-state memory device comprises a first partition and a second partition, wherein the program code is stored in the first partition, and wherein data read or stored by the program code is read or stored, respectively, in the second partition.

11. The invention of Claim 10, wherein the first partition is fixed.

12. The invention of Claim 1 or 6, wherein the program code comprises an application selected from the group consisting of an image viewer, an audio player, a calendaring tool, a word processor, a game, and a presentation program.

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13. The invention of Claim 1 or 6, wherein the program code can be used only for a predetermined amount of time.

14. The invention of Claim 1 or 6, wherein the program code can be used only for a predetermined number of uses.

15. The invention of Claim 1 or 6, wherein the program code is operative to store data only in the solid-state memory device.

16. The invention of Claim 1 or 6, wherein the program code is encrypted with an identifier of the solid-state memory device.

17. The invention of Claim 1 or 6, wherein the solid-state memory device comprises a three-dimensional memory array.

18. The invention of Claim 17, wherein the memory array comprises memory cells of a semiconductor material.

19. The invention of Claim 17, wherein the memory array comprises write-once memory cells.

20. A method for using a solid-state memory device storing program code, the method comprising:

(a) connecting a solid-state memory device with a host device, the solid-state memory device storing encrypted program code and an identifier associated with the solid-state memory device;

(b) providing the encrypted program code and the identifier to the host device;
and

(c) with the host device, decrypting the encrypted program code using the identifier.

21. The invention of Claim 20 further comprising:
- (d) with the host device, performing at least one of the following acts:
 - (d1) reading data stored in the solid-state memory device using the
5 decrypted program code;
 - (d2) storing data in the solid-state memory device using the decrypted
program code.
22. The invention of Claim 20, wherein (b) comprises automatically providing the
10 encrypted program code and the identifier to the host device.
23. A solid-state memory device comprising:
a first portion storing an identifier associated with the solid-state memory device;
and
a second portion storing encrypted program code that can be decrypted with a host
15 device connected with the solid-state memory device using the stored identifier.
24. The invention of Claim 23, wherein, after being decrypted, the program code is
operative to enable the host device connected with the solid-state memory device to
perform at least one of the following acts: read data stored in the solid-state memory
20 device using the program code or store data in the solid-state memory device using the
program code.
25. The invention of Claim 20 or 23, wherein the identifier is unique to the solid-state
25 memory device.
26. The invention of Claim 20 or 23, wherein the program code is written in a
hardware-independent language.

27. The invention of Claim 26, wherein the hardware-independent language comprises Java.

28. The invention of Claim 20 or 23, wherein the solid-state memory device comprises a first partition and a second partition, wherein the encrypted program code is stored in the first partition, and wherein data read or stored by the decrypted program code is read or stored, respectively, in the second partition.

29. The invention of Claim 28, wherein the first partition is fixed.

30. The invention of Claim 20 or 23, wherein the program code comprises an application selected from the group consisting of an image viewer, an audio player, a calendaring tool, a word processor, a game, and a presentation program.

31. The invention of Claim 20 or 23, wherein the program code can be used only for a predetermined amount of time.

32. The invention of Claim 20 or 23, wherein the program code can be used only for a predetermined number of uses.

33. The invention of Claim 20 or 23, wherein the program code is operative to store data only in the solid-state memory device.

34. The invention of Claim 20 or 23, wherein the solid-state memory device comprises a three-dimensional memory array.

35. The invention of Claim 34, wherein the memory array comprises memory cells of a semiconductor material.

36. The invention of Claim 34, wherein the memory array comprises write-once memory cells.

5 37. A method for using a solid-state memory device storing program code, the method comprising:

(a) connecting a solid-state memory device with a host device, the solid-state memory device storing program code;

10 (b) providing the program code to the host device, the program code being operative to store data only in the solid-state memory device; and

(c) with the host device, storing data in the solid-state memory device using the program code.

15 38. The invention of Claim 37, wherein (b) comprises automatically providing the program code to the host device.

20 39. A solid-state memory device comprising:

a first portion storing program code; and

a second portion;

wherein the program code is operative to enable a host device connected with the solid-state memory device to store data only in the second portion of the solid-state memory device using the program code.

25 40. The invention of Claim 37 or 39, wherein the solid-state memory device comprises a write-once memory device, whereby an amount of data than can be stored in the solid-state memory device is limited.

41. The invention of Claim 37 or 39, wherein the program code is written in a hardware-independent language.

42. The invention of Claim 41, wherein the hardware-independent language comprises Java.

43. The invention of Claim 37 or 39, wherein the solid-state memory device comprises a first partition and a second partition, wherein the program code is stored in the first partition, and wherein data stored by the program code is stored in the second partition.

44. The invention of Claim 43, wherein the first partition is fixed.

45. The invention of Claim 37 or 39, wherein the program code comprises an application selected from the group consisting of an image viewer, an audio player, a calendaring tool, a word processor, a game, and a presentation program.

46. The invention of Claim 37 or 39, wherein the program code can be used only for a predetermined amount of time.

47. The invention of Claim 37 or 39, wherein the program code can be used only for a predetermined number of uses.

48. The invention of Claim 37 or 39, wherein the program code is encrypted with an identifier of the solid-state memory device.

49. The invention of Claim 37 or 39, wherein the solid-state memory device comprises a three-dimensional memory array.

50. The invention of Claim 49, wherein the memory array comprises memory cells of a semiconductor material.

51. The invention of Claim 49, wherein the memory array comprises write-once memory cells.

5 52. A method for distributing program code stored in a solid-state memory device comprising a three-dimensional array of memory cells, the method comprising:

(a) storing program code in a solid-state memory device comprising a three-dimensional array of memory cells; and

(b) distributing the solid-state memory device.

10 53. The invention of Claim 52, wherein the program code comprises an executable software application.

54. The invention of Claim 52, wherein act (a) is performed by a manufacturer of the program code.

55. The invention of Claim 52, wherein (b) comprises selling the solid-state memory device to a user.

20 56. The invention of Claim 52 further comprising:

storing the software application in an additional plurality of solid-state memory devices each comprising a respective three-dimensional array of memory cells; and

distributing the additional plurality of solid-state memory devices.

25 57. The invention of Claim 52, wherein the memory cells are arranged in a plurality of vertically-stacked layers of memory cells.

58. The invention of Claim 52, wherein each memory cell comprises exactly two terminals.

59. The invention of Claim 52, wherein the three-dimensional memory array comprises a plurality of wires comprising wordlines and bitlines, and wherein each memory cell is connected to exactly two wires: the respective wordline and the respective bitline.

60. The invention of Claim 52, wherein the memory cells comprise a semiconductor material.

61. The invention of Claim 52, wherein the memory cells comprise write-once memory cells.

62. The invention of Claim 52, wherein the program code is written in a hardware-independent language.

63. The invention of Claim 62, wherein the hardware-independent language comprises Java.

64. The invention of Claim 52, wherein the program code is operative to enable a host device coupled with the solid-state memory device to perform at least one of the following acts: read data stored in the solid-state memory device using the program code or store data in the solid-state memory device using the program code.

65. The invention of Claim 64, wherein the solid-state memory device comprises a first partition and a second partition, wherein the program code is stored in the first partition, and wherein data read or stored by the program code is read or stored, respectively, in the second partition.

66. The invention of Claim 65, wherein the first partition is fixed.

67. The invention of Claim 52, wherein the program code comprises an application
selected from the group consisting of an image viewer, an audio player, a calendaring
tool, a word processor, a game, and a presentation program.

68. The invention of Claim 52, wherein the program code can be used only for a
predetermined amount of time.

69. The invention of Claim 52, wherein the program code can be used only for a
predetermined number of uses.

70. The invention of Claim 52, wherein the program code is operative to store data
only in the solid-state memory device.

71. The invention of Claim 52, wherein the program code is encrypted with an
identifier of the solid-state memory device.